



## I. COURSE DESCRIPTION:

This project and research oriented course is intended to develop the students ability to apply design and analysis techniques and reporting skills to project and research oriented tasks.

## II. LEARNING OUTCOMES AND ELEMENTS OF THE PERFORMANCE:

Upon successful completion of this course, the student will demonstrate the ability to:

### 1. ***Understand the principles and characteristics of solid state semiconductor switches.***

#### Potential Elements of the Performance:

- Sketch the current, voltage and power waveforms of a generic switch
- Classify semiconductor switches as minority, majority and mixed carrier devices
- Draw the output characteristics of MOSFETs, IGBTs, power BJTs and Schottky diodes
- Design, operate and test generic solid state switches with inductive load

### 2. ***Use Hybrid parameters for BJTs***

#### Potential Elements of the Performance:

- Write the hybrid defining equations
- Convert common emitter hybrid parameters to CC and CB parameters
- Calculate input and output impedance and gain based on hybrid parameters
- Use MathCad, a computer method for gain and impedance calculations

### 3. ***Specify heat sinks for semiconductors***

#### Potential Elements of the Performance:

- Measure the thermal resistance of a heat sink in the laboratory
- Calculate the dissipated power for a given semiconductor
- Calculate the required thermal resistance for a given amount of dissipated power
- Determine the static and transient temperature rise for a semiconductor switch

**4. *Design Linear Voltage Regulators***

Potential Elements of the Performance:

- Be familiar with Shunt and Series voltage regulators
- Use simple and fold back short circuit protection circuits
- Design digitally controlled voltage regulators
- Design voltage regulators using Linear Integrated Circuits.

**5. *Understand and design DC to DC converters***

Potential Elements of the Performance:

- Be familiar with Shunt and Series voltage regulators
- Use simple and fold back short circuit protection circuits
- Design digitally controlled voltage regulators
- Design voltage regulators using Linear Integrated Circuits.

**6. *Design isolated switched mode power supplies (SMPS)***

Potential Elements of the Performance:

- Sketch the schematics of Flyback and Forward
- Test an existing flyback converter, using a digital storage oscilloscope and current probes
- Identify schematic diagrams of Push-Pull, Half-Bridge and Full-Bridge power supplies.

**7. *Research a topic in Power Converter design and build a prototype Power Converter.***

Potential Elements of the Performance:

- Complete a Library search and an Internet search to collect technical information.
- Write a Technical Report on the state of the art of Switched mode or Linear power converters.
- Use a computer method to perform your design.
- Apply prototyping methods to construct and test a working Power converter

**III. TOPICS:**

1. Review and classification of power electronic components
2. Characteristics and construction of solid state switches
3. Hybrid parameters
4. Heat sink design
5. Printed circuit board design and prototype manufacturing
6. Linear voltage regulators
7. Switched mode voltage regulators

**IV. REQUIRED RESOURCES/TEXTS/MATERIALS/SAFETY:**

1. Circuit Design Study Material notes and Parts Kits are sold in the Bookstore.
2. Laboratory handouts will be provided by the teacher.
2. MathCad , PSPICE, Eagle and IsoPro software is available in the computer room (B1015) and also in B1010, B1020 and B1070.
3. It is recommended, that the student downloads a free version of the above software
4. Motorola AN920A and component specifications for each electronic part in the Parts Kit: Please perform a Google search and download each data sheet.
5. Safety goggles: bring your own.
6. Surgical rubber gloves: bring your own.

**SAFETY IN THE LABORATORY**

Any testing in the laboratory, when open frame experimental designs or even CSA approved equipment are connected to 120volts ac power, is done in the presence of the teacher or laboratory technologist.

During chemical etching, CNC machine cutting, soldering, or using any available power tools, safety goggles are worn.

Etching in the Chemistry Laboratory is strictly performed under the supervision of the teacher or laboratory technologist. Rubber surgical gloves must be worn.

When working in the afternoon, all activities are restricted to computer research, or board level testing, when the experiment is powered from a low voltage (less than 24volts) DC laboratory or bench power supply.

## V. EVALUATION PROCESS/GRADING SYSTEM:

Grading is based on three written Tests , six Laboratory Experiments and a Research Project. Each component has the following weight:

50% Theory + 25% Laboratory + 25% Research = 100%

In order to pass the course, each of the three components must be individually passed. The Research Report and Project is a continuously ongoing activity. A working prototype of the assigned research topic must be completed and demonstrated no later than April 16, 2009. The written research Report is handed in on the same date. Late Research Reports are only marked pass/fail ( D or F.)

The following semester grades will be assigned to students in postsecondary courses:

<b>Grade</b>	<b>Definition</b>	<i>Grade Point Equivalent</i>
A+	90 – 100%	4.00
A	80 – 89%	3.00
B	70 - 79%	2.00
C	60 - 69%	1.00
D	50 – 59%	0.00
F (Fail)	49% and below	
CR (Credit)	Credit for diploma requirements has been awarded.	
S	Satisfactory achievement in field /clinical placement or non-graded subject area.	
U	Unsatisfactory achievement in field/clinical placement or non-graded subject area.	
X	A temporary grade limited to situations with extenuating circumstances giving a student additional time to complete the requirements for a course.	
NR	Grade not reported to Registrar's office.	
W	Student has withdrawn from the course without academic penalty.	

## VI. SPECIAL NOTES:

### Disability Services:

If you are a student with a disability (e.g. physical limitations, visual impairments, hearing impairments, or learning disabilities), you are encouraged to discuss required accommodations with your professor and/or the Disability Services office. Visit Room E1101 or call Extension 2703 so that support services can be arranged for you.

### Retention of Course Outlines:

It is the responsibility of the student to retain all course outlines for possible future use in acquiring advanced standing at other postsecondary institutions.

### Communication:

The College considers **WebCT/LMS** as the primary channel of communication for each course. Regularly checking this software platform is critical as it will keep you directly connected with faculty and current course information. Success in this course may be directly related to your willingness to take advantage of the **Learning Management System** communication tool.

### Plagiarism:

Students should refer to the definition of “academic dishonesty” in *Student Code of Conduct*. Students who engage in academic dishonesty will receive an automatic failure for that submission and/or such other penalty, up to and including expulsion from the course/program, as may be decided by the professor/dean. In order to protect students from inadvertent plagiarism, to protect the copyright of the material referenced, and to credit the author of the material, it is the policy of the department to employ a documentation format for referencing source material.

### Course Outline Amendments:

The professor reserves the right to change the information contained in this course outline depending on the needs of the learner and the availability of resources.

**VII. PRIOR LEARNING ASSESSMENT:**

Students who wish to apply for advance credit transfer (advanced standing) should obtain an Application for Advance Credit from the program coordinator (or the course coordinator regarding a general education transfer request) or academic assistant. Students will be required to provide an unofficial transcript and course outline related to the course in question.

Credit for prior learning will also be given upon successful completion of a challenge exam or portfolio.